EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	392	level$shifter	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/19 18:59
S2	8	S1 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:34
S3	1	S1 same (clock$3 near6 delay$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/19 18:59
S4	3523	level$shift$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:33
S5	17	(S4 and intel.as.) not S2	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/19 18:59
S6	35194	(level voltage) adj2 shift$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:34
S7	229	S6 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:34
S8	392	level$shifter	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:35
S9	8	S8 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:35
S10	3523	level$shift$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:35
S11	17	(S10 and intel.as.) not S9	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:35

S12	204	S7 not S9 not S11	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:36
S13	1285	S6 with delay$	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:35
S14	1285	S13 not S9 not S11	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:36
S15	10	(S13 and intel.as.) not S9 not S11	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:58
S16	5156	i/o adj1 buffer$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:55
S17	886	core near2 latch$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:56
S18	4	S16 same S17	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:56
S19	130	S16 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:59
S20	811	S16 with (clock$3 enabl$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:58
S21	38	S20 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 12:59
S22	30508	(i/o core) same (latch$3 master slave (flip$flop$1) ff$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:19
S23	585	S22 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:20

S24	8635	(i/o) near10 (latch$3 master slave (flip$flop$1) ff$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:19
S25	5562	(core) near10 (latch$3 master slave (flip$flop$1) ff$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:20
S26	110	S24 same S25	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:20
S27	6	S26 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:22
S28	200	S24 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:22
S29	152	S25 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:24
S30	424	(i/o core) same ((master near10 (latch$3 (flip$flop$1) ff$1)) (slave near10 (latch$3 (flip$flop$1) ff$1)))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:24
S31	25	S30 and intel.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/10/20 13:24